

ABSTRACT OF THE DISCLOSURE

A method for making a combination IC card comprising the steps of forming antenna leads and patterned connection leads, forming a hole for mounting an external electrode chip on a sheath on at least one side of the antenna substrate, mounting an IC chip on the antenna substrate having the antenna leads and the patterned connection leads, mounting the external electrode chip on the antenna substrate having the antenna leads and the patterned connection leads, and attaching the sheath having the hole on the antenna substrate.